

FIG. 1

Adder 100

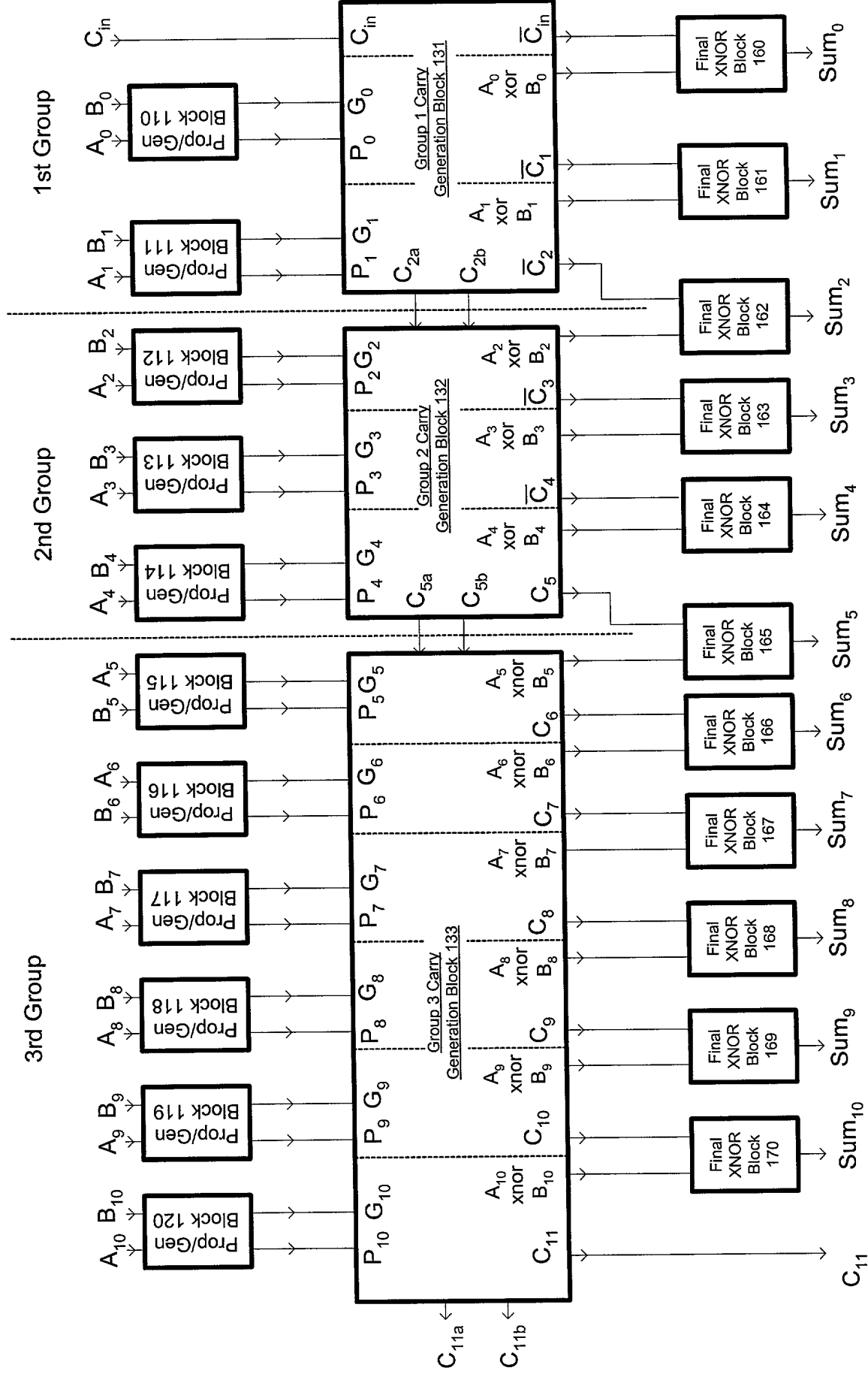
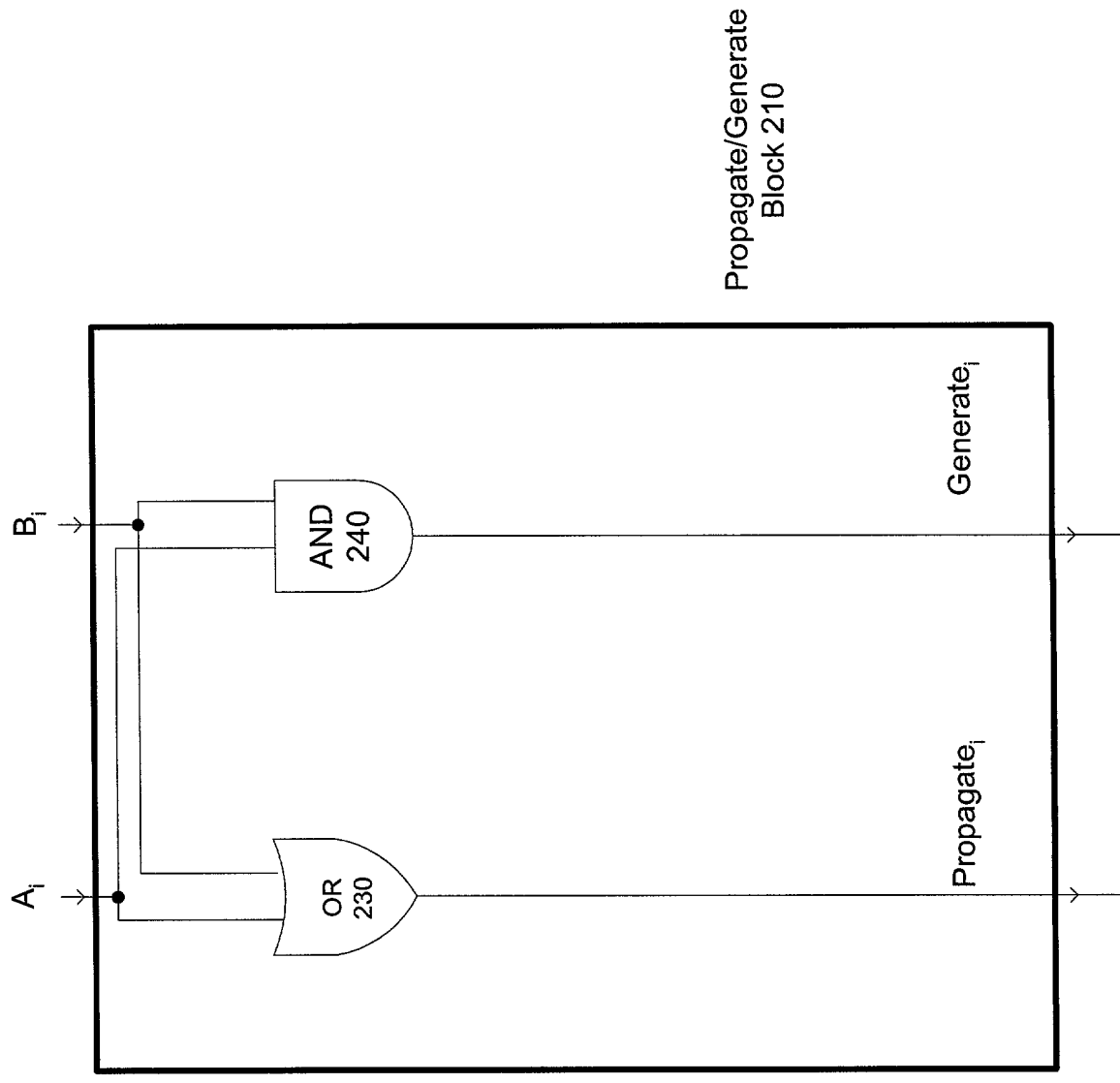


FIG. 2



Group 1 Carry  
Generation Block 131

1st Stage  
of 1st Group 351

2nd Stage  
of 1st Group 352

3rd Stage  
of 1st Group 353

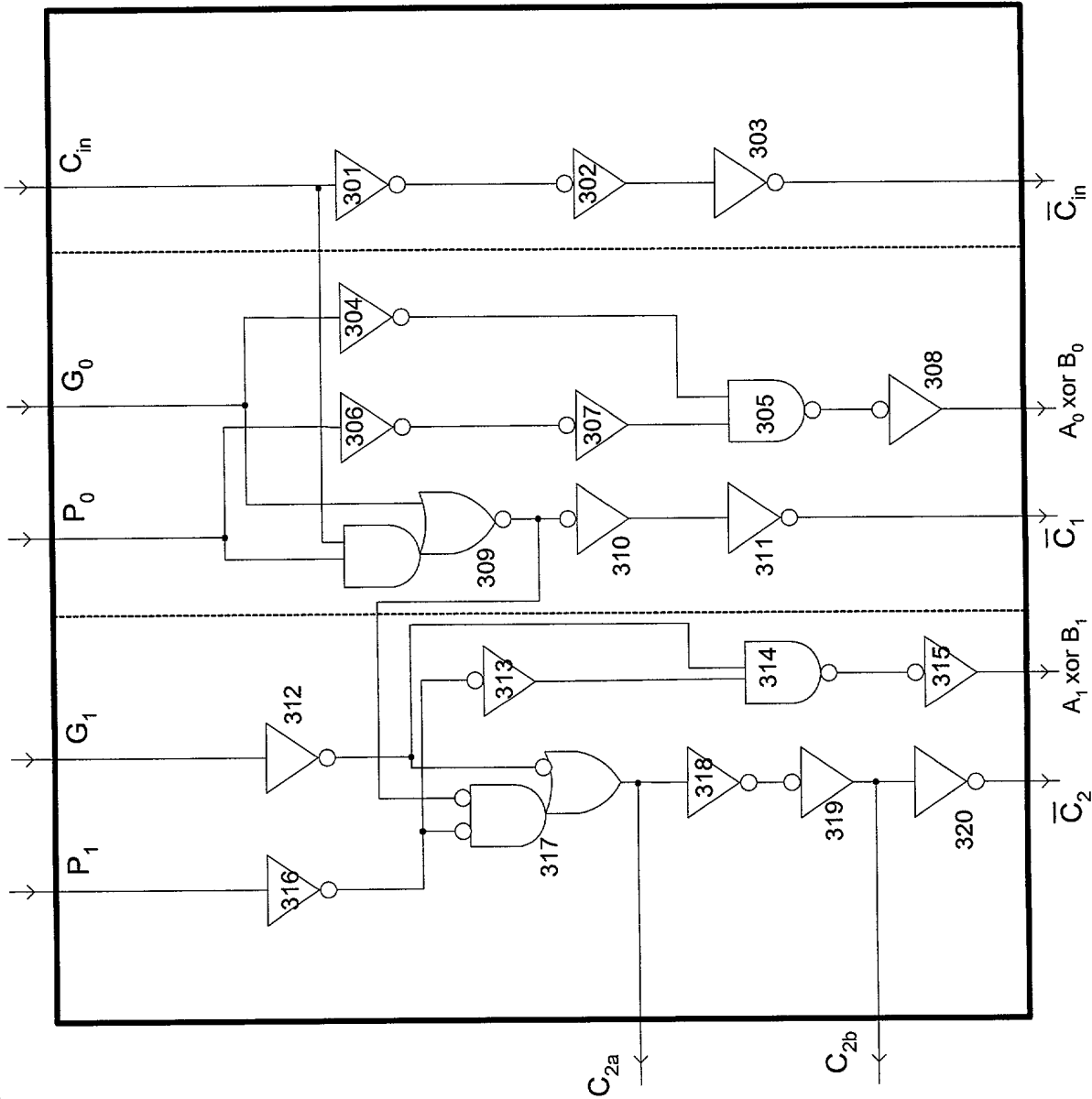


FIG. 3

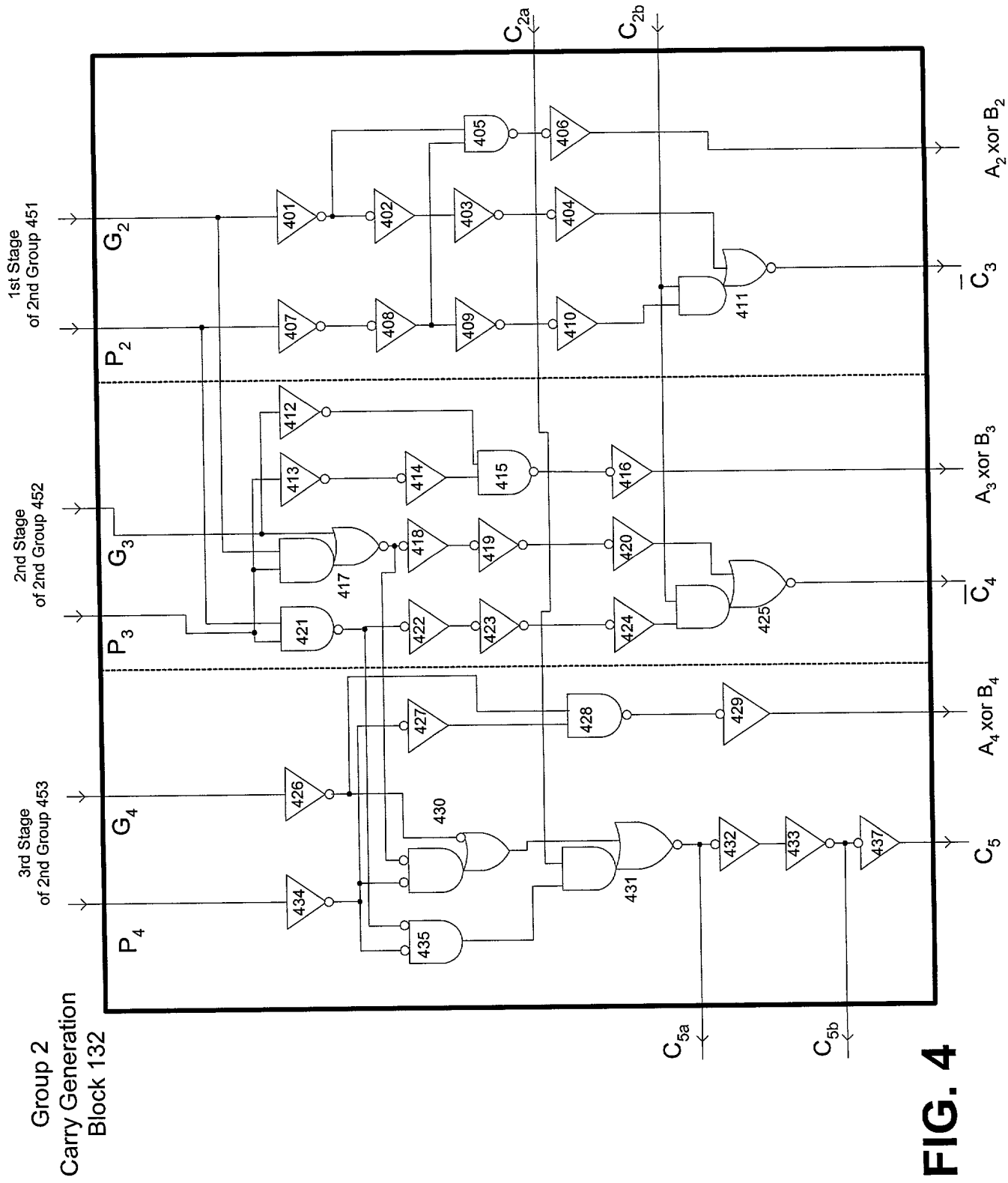
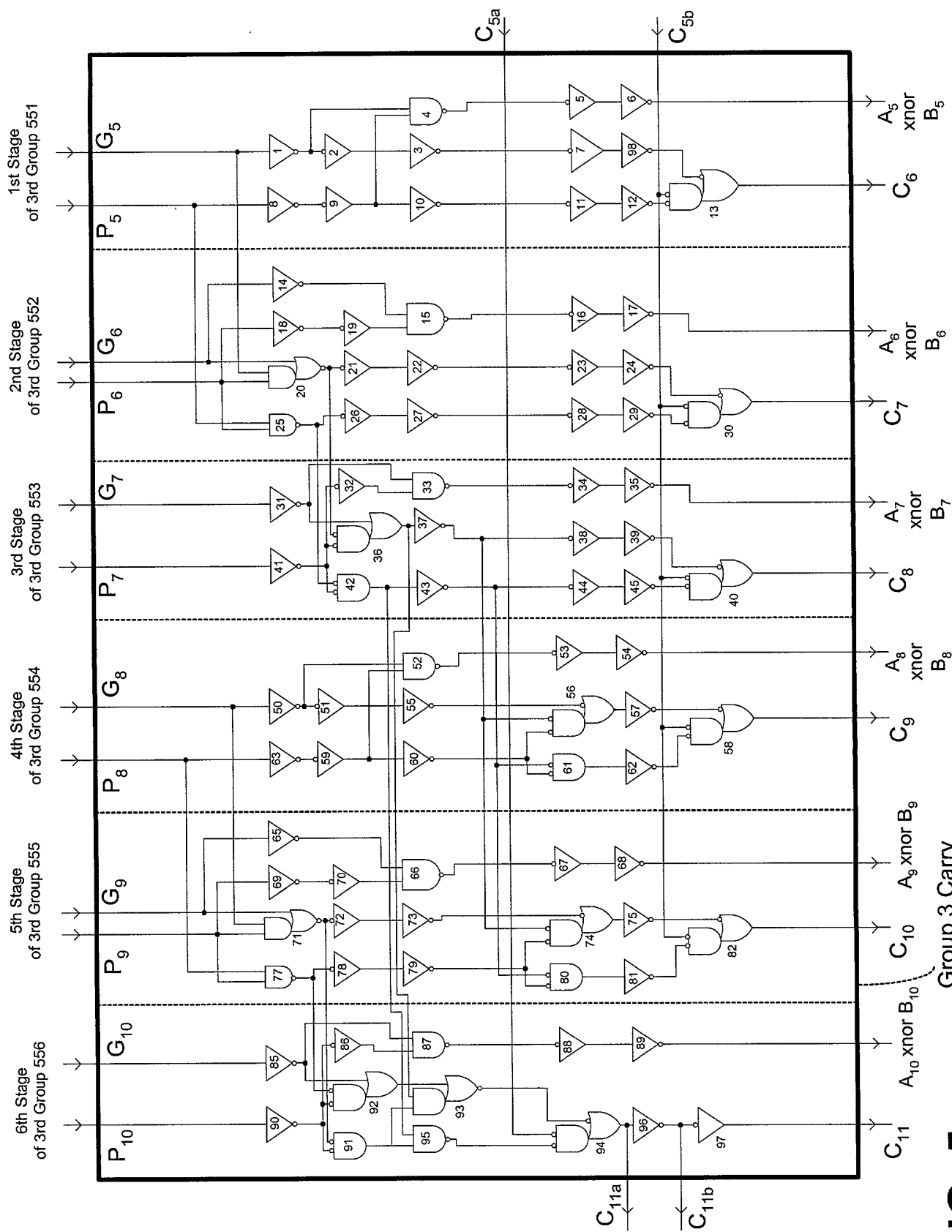


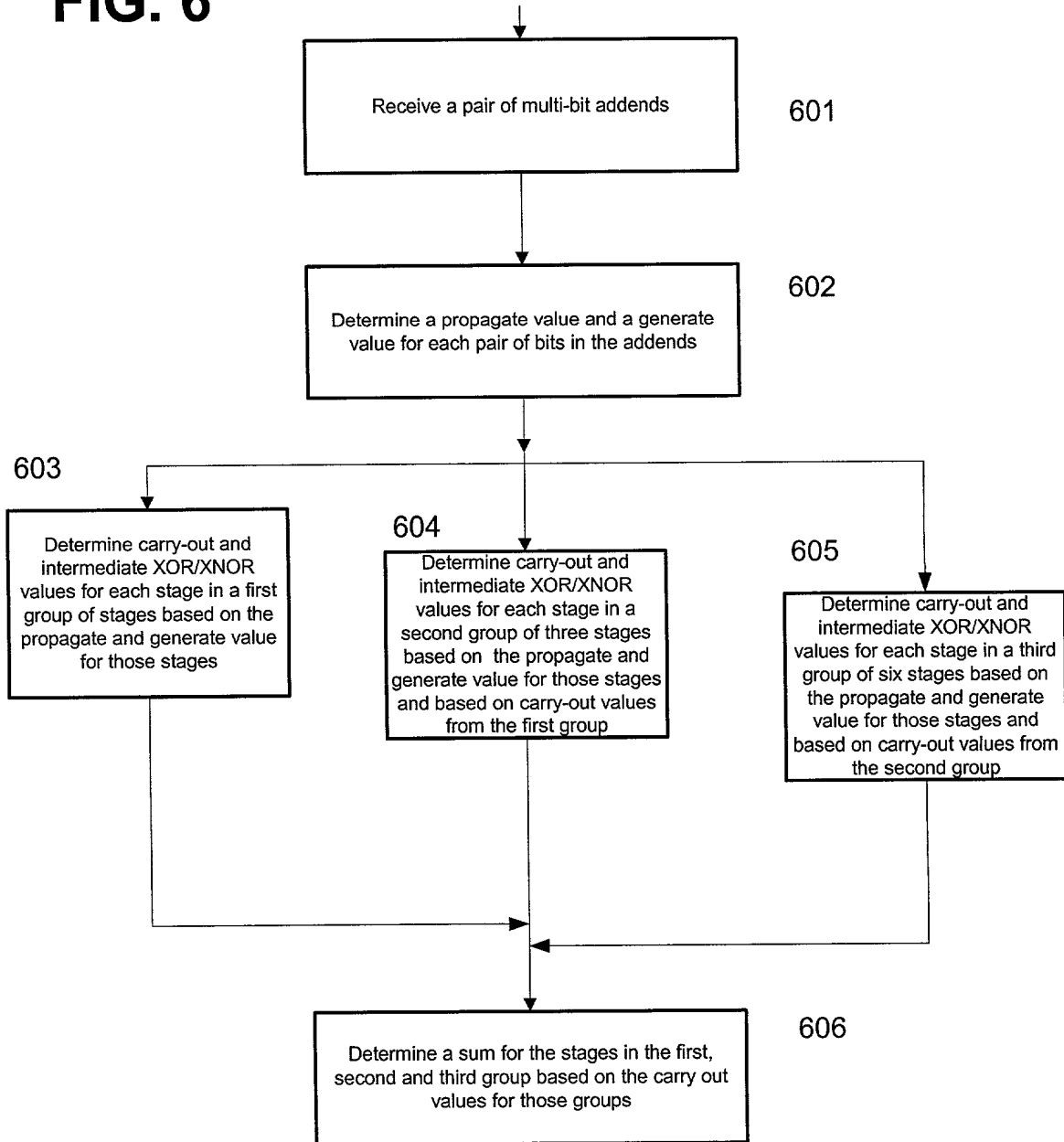
FIG. 4



Group 3 Carry  
Generation Block 133

**FIG. 5**

**FIG. 6**



And-Or-Invert Gate  
431

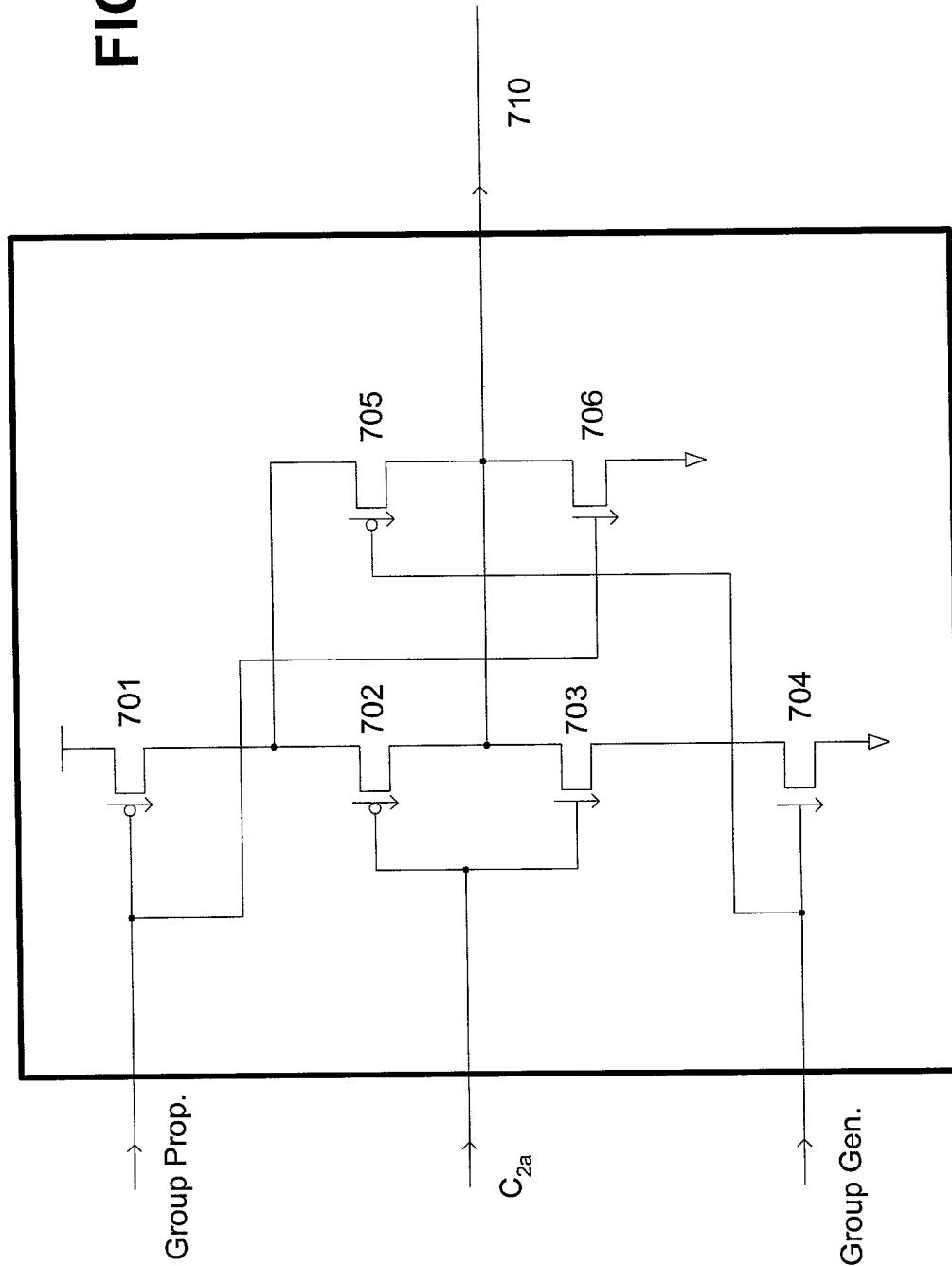


FIG. 7

AND-OR-INVERT Gate  
309

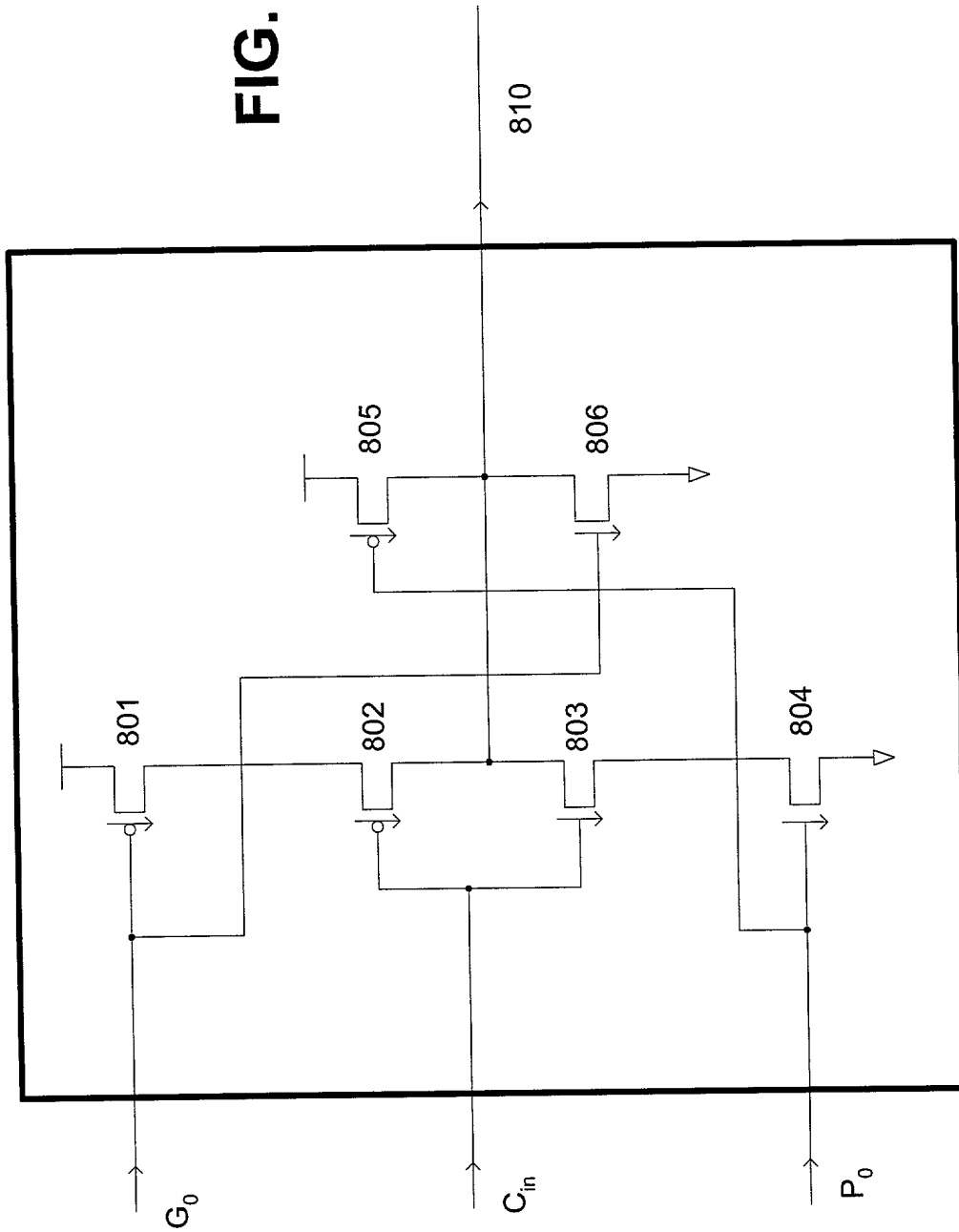


FIG. 8